BIO-DATA

Name	L.M. Patnaik
Former Designation	Former Professor, Indian Institute of Science, Bangalore Former Vice Chancellor Defence Institute Advanced Technology (Govt of India, Min of Def, DRDO) Deemed University, Pune
Current Designation	INSA Senior Scientist and Adjunct Professor National Institute of Advanced Studies & Honorary Professor, Department of Electronics Systems Engineering, Indian Institute of Science, Bangalore 560012
Current Address	Apt. No. 313, Sriniket Apartments, 19/8 First Cross, MSR College Road, Mathikere, Bangalore 560054
Phone	09845434196 (Mob)
Email	lalitblr@gmail.com, patnaiklm@yahoo.com
Date of Birth	22 July 1946

Academic Qualifications (First Degree Onwards)

Examination	University/Institution	Class and Rank	Year
D.Sc. in	Indian Institute of		1989
Engineering	Science		
Faculty	Bangalore		
Thesis : I	nvestigations on Computer Sy	stems and Architectures	
Ph.D in	Indian Institute of		1978
Engineering	Science		
Faculty	Bangalore		
Thesis : Optimizati	on and Multivariable Compute	er Control of an Ammonia	Reactor
M.E. in	Indian Institute of	Distinction	1971
Electrical	Science		
Engineering	Bangalore		
B.Sc. (Engg.) in	Sambalpur University	First Class	1969
Electrical	Sambalpur, Orissa	Honours,	
Engineering		First Rank in the	
		University	

Details of Service:

From		То		Designation	
Month	Year	Month	Year		
Mar	2008	August	2011	Vice Chancellor, Defence	
				Institute of Advanced	
				Technology, Deemed	
				University, Girinagar, Pune	
July	1986	Mar	2008	Professor, Dept of Computer	
-				Science and Automation,	
				IISc, Bangalore	
				Convener, Computational	
				Neurobiology Group, since	
				April 2003, IISc, Bangalore	
October	1983	June	1986	Associate Professor, IISc,	
				Bangalore	
April	1980	September	1983	Assistant Professor, IISc,	
-				Bangalore	
April	1976	March	1980	Systems Programmer, IISc,	
_				Bangalore	
October	1971	March	1976	Senior Research Assistant,	
				IISc, Bangalore	

Significant Contribution made as Vice Chancellor at DIAT, Pune

At the Defence Institute of Advanced Technology, he has been directing the unique academic, research, and training activities in the broad areas of defence science and technology, with a wide spectrum of participation of senior officers from DRDO, Army, Air Force, Navy, Indian Ordnance Factories, Directorate General of Quality Assurance, public and private sector organisations, several friendly foreign countries such as Ethiopia, Burma, Sri Lanka, Bhutan, Singapore, Malaysia and civilian students from general public. His specific initiatives to strengthen defence education and research at this University are in the areas of initiation of collaborative efforts at the International level, organisation of several long and short courses and International Conferences of specific relevance to Indian defence forces and organisations, nurturing a conducive research environment in the areas of defence science and technology, and initiation of new Postgraduate programs of relevance to defence technologies such as cyber security, materials science and engineering for defence applications. The University offers only Master's and Doctoral programs; other than a number of short and long courses for armed forces and defence scientists/technologists.

Guidance of Students for Research Conferments

Degree	Completed	Under Progress	Total
Ph.D.	24	7	31
M.Sc (Engg.)	18	-	18

Research Publications

	Papers in Referred Journals	Papers in Referred Conference Proceedings	Books	Chapter in Books	Technical Reports	Total
Published/ Accepted for Publications	372	656	22	12	29	1091

Academic Recognitions

Fellowship

- 1. Fellow, The Institute of Electrical and Electronics Engineers (IEEE), U.S.A.
- 2. Fellow, The World Academy of Sciences(TWAS), Trieste, Italy.
- 3. Fellow, The Computer Society of India.
- 4. Fellow, Indian National Academy of Engineering.
- 5. Fellow, Indian National Science Academy, New Delhi, India.
- 6. Fellow, Indian Academy of Sciences, Bangalore, India.
- 7. Fellow, The National Academy of Sciences, Allahabad, India.
- 8. Honorary Fellow, The Indian Society for Technical Education.
- 9. Fellow, The Institution of Electronics and Telecommunication Engineers, India.
- 10. Fellow, The Institution of Engineers (India).
- 11. Fellow, Maharashtra Academy of Sciences.

Awards

- 1. M N Sha Memorial Award for Best Application Oriented Paper, IETE, 2016
- 2. Outstanding Contribution to Soft Computing Award of the International Neural Network Society India Chapter , 2014
- 3. Distinguished Alumnus Award 2012, National Institute of Technology, Rourkela.
- 4. Top Management Consortium, Pune, Education Defence Services Excellence Award 2009, Mar 2010.
- 5. Biju Patnaik Award for Excellence in Science 2007 (Award given in 2009).
- 6. Srinivasa Ramanujan Birth Centenary Award, Indian Science Congress Association, 2008.
- 7. Honorary Fellow, Indian Society for Technical Education, 2006.
- 8. Goyal Prize for Applied Science, 2005.
- 9. Distinguished Engineer Award, Institution of Engineers (India), Karnataka State Centre, 2004.
- 10. Alumni Award for Excellence in Research for Engineering, Indian Institute of Science, 2003.
- 11. Awarded the IEEE Computer Society's Meritorious Service Award, 2002.
- 12. FICCI Award for Innovation in Material Science, Applied Research and Space Science, 2001-2002.
- 13. Om Prakash Bhasin Award, Electronic and Information Technology, 2001.
- 14. Indira Gandhi Sadhbavna Award of the National Integration and Economic Council, New Delhi, Nov 2001.
- 15. Pandit Jawaharlal Nehru National Award for Engineering and Technology, 1999.
- 16. Bharat Jyoti Award of the India International Friendship Society, New Delhi, September 2001.
- 17. First SVC Aiya Memorial Trust Award for Telecommunication Education, 2000.
- 18. Fourth Sir C V Raman Memorial Lecture, The Institution of Electronics and Telecommunication Engineers, 2000.
- 19. Awarded the IEEE Computer Society's 1999 Technical Achievement Award for contributions in the field of parallel, distributed, and soft computing and high performance genetic algorithms.

- 20. Awarded the "Distinguished Professional Engineer", Award of the Computer Engineering Division of the Institution of Engineers(India), 1999.
- 21. Awarded the "Hands for Indian IT Award" for the year 1998 in recognition of contributions to the Indian IT industry.
- 22. Awarded the "VASVIK Award" for 1996 in the field of Electronic Sciences and Technology.
- 23. Awarded the Certificate of Appreciation "For excellent service and dedication to the objectives of the IEEE Computer Society as a Distinguished Visitor", 1995.
- 24. Awarded "Samant Chandrasekhar Award" for the year 1994 from the Government of Orissa as the outstanding Oriya Scientist outside Orissa.
- 25. Platinum Jubilee Lecture Award (Computer Science), Indian Science Congress, 1994.
- 26. Awarded "Shri Hari Om Ashram Prerit Dr. Vikram Sarabhai Research Award" for the year 1989 for achievements in the field of Electronics, Informatics, Telematics and Automation.
- 27. Awarded "Fifteenth Ram Lal Wadhwa Award" of the Institution of Electronics and Telecommunication Engineers for the year 1992 for contributions in the field of Electronics and Telecommunications during the last 10 years.
- 28. Awarded Honourable Mention Award for the paper titled Simulated Annealing-Based Channel Routing on Hypercube Computers, presented at the Fourth CSI/IEEE International Symposium on VLSI Design, 1991.
- 29. Recipient of the Best Alumnus Award from the Regional Engineering College, Rourkela for outstanding contributions in the areas of Automation and Computer Science.
- 30. State level awards for securing the first rank in the High School Certificate examination in the State of Orissa.
- 31. Recipient of National Merit Scholarship on the basis of the above examination result.

Academic Honours

- 1. Vice President, Indian National Academy of Engineering (INAE), 2007-2009.
- 2. President, Computational Intelligence Society of India, 2003.
- 3. President, Advanced Computing and Communications Society, 2002.
- 4. President, Physical Sciences Section, The National Academy of Sciences, 2000.
- 5. Listed in Who's Who in the World, Marquis Publications, Eighteenth Edition, December 2000.
- 6. Listed in Who's Who in Asia and The Pacific Nations, Fourth Edition, 1999.
- 7. Listed in Who is Who in the World, Marquis Publications, Sixteenth Edition, 1999.
- 8. "Senior Associate", National Institute of Advanced Studies, Bangalore.
- 9. IEEE Asia-Pacific Distinguished Visitor, 1994-95, 1996-98.
- 10. IEEE-CS Distinguished Lecturer for the Asia-Pacific Region.
- 11. Distinguished Visitor, CSI Annual Convention and Exhibition, September-October 1991, New Delhi.
- 12. Listed in Reference Orissa, Who's Who of Men & Women of Achievement in Orissa.
- 13. Listed in Who's Who in the World, Marquis Publications, Eighth Edition, U.S.A.
- 14. Listed in Reference Asia, Asia's Who's Who of Men and Women of Achievement, Fifth Volume, 1989.
- 15. Listed in the Directory of Distinguished Computer Professionals of India, Institute of Chartered Computer Professionals of India, Secunderabad, 1990.
- 16. First IEEE Region 10 Distinguished Lecturer, 1990.

Significant Research Contributions

- 1. Computer Architecture
- 2. High Performance Computing
- 3. CAD of VLSI Systems
- 4. Computational Neuroscience
- 5. Mobile Computing

6. Soft Computing

7. Real-Time Systems

Prof. Patnaik has made several pioneering contributions in the broad discipline of Computer Science and Engineering. His outstanding research work spans several areas such as High Performance Computing, Soft Computing, Computer Aided Design of VLSI Circuits, Mobile Computing, Computational Neuroscience, Medical Imaging, Information Retrieval and Security including Data and Web Mining. A brief summary of his significant research results is presented below.

High Performance Computing:

One of his pioneering contributions has been the design of an intelligent configuration of hierarchical network of hypercubes called the Extended Hypercube (EH), a highly efficient message passing architecture. Accelerated neural network simulators have been developed using this novel architecture, to yield better performance than some of the best commercial approaches. He has developed modified version of Pixel-based reconstruction (PBR) algorithm for three-dimensional medical imaging and ported this algorithm onto the above

EH architecture to yield excellent performance improvement. He has played a significant role in evaluating the performance of India's supercomputer PARAM.

Soft Computing:

The focus of Prof. Patnaik's original research in this area has been on theoretical and practical fronts.

He has developed a novel exact model for analyzing the working of Genetic Algorithms (GAs), when the objective function is a function of unitation and population is infinite. The concept of fitness of moments developed by him to analyze the working of GAs and characterizing their dynamics is ingenious. His original concept of adaptive probabilities of crossover and mutation to realize efficient multimodal function optimization has been extensively used in VLSI channel routing, testing, and physical design. For the first time, he has applied genetic programming to multicategory pattern classification.

His recent work on computational neuroscience has been widely acclaimed by neuroscientists and doctors. He has intelligently used several pattern classification techniques and feature extraction methods to classify MRI, fMRI images and EEG signals to diagnose several brain disorders and carry out cognition studies.

Mobile Computing:

He has developed a significantly novel distributed channel access protocol that occupies the channel reservation and the iterative/global transmission power control schemes in wireless adhoc networks. His novel energy-efficient routing algorithms, address allocation strategies, resource allocation policies, and location tracking techniques developed for wireless networks are widely acclaimed by researchers and industry practitioners.

Computer Aided Design of VLSI Circuits:

He has developed a novel test-set selection technique based on the frequency domain testing of analog circuits. This novel technique is best suited for use of existing building blocks in systems-on-chip for implementation of an on-chip test-signal generator and test response analyzer. For digital circuit testing, he has proposed a new coverage metric for delay fault tests. He has developed novel parallel algorithms and architectures for placement, routing, circuit simulation, and testing of VLSI circuits.

Real -Time Systems and Control:

Prof. Patnaik's applied skills in the area of real-time multivariable computer control have been demonstrated through his novel direct digital and supervisory control algorithms for ammonia reactors in fertilizer plants, steel, and nuclear industries. His self-tuning control algorithms yield improved performance for process industries.

Some of his noteworthy projects for the strategic sectors in India are, Real-Time Scheduling Algorithms for Onboard Computers for Indian Satellite Launch Vehicles, Automatic Target (Missile) Recognition, Multi Sensor Data Fusion for Onboard Radars, Space Time Adaptive Processing for Onboard Radar Signal Processing, and Online Nuclear Fuel Rod Positioning.

Computational Neuroscience:

Prof. Patnaik has developed techniques to classify MRI images for neurological disorders such as Parkinson's, Alzheimer's and epilepsy using improved algorithms based on support vector machines, neural networks and wavelets. His work in these areas is based on direct interaction with medical specialists from AIIMS and NIMHANS. He has also carried out novel cognition-related studies for motor control and other behavioral issues, using fMRI images.

Member of the Editorial Boards

A. International

- Editor, International Journal of Management Science ad Engineering Management, London, UK, 2010.
- Member, Editorial Board of the Journal of Computers (JCP, ISSN 1796-203X), Finland.
- Editor, International Journal of Distributed Sensor Networks, Taylor and Francis/CRC Press.
- Member, Editorial Board, International Journal of Computer Science and Applications, Technomathematics Research Foundation.
- Member, Editorial Board, Journal of Information and Communication Technology, PeCOP Resources (Nig.) Ltd., Africa.
- Member, Editorial Board, Journal of Web Engineering, Rinton Press.
- Member, Editorial Board, Parallel And Distributed Computing Practices Journal, NOVA Science Publishers.
- Member, Editorial Board, Evolutionary Optimization An International Journal, Kluwer Publishing Co.
- Associate Editor, IEEE Transactions on VLSI Systems, IEEE Computer Press.
- Member, Editorial Board, The Computer Journal, Oxford University Press for the British Computer Society.
- Member, Editorial Board, Parallel Algorithms and Applications, Gordon and Breach Publishers.
- Member, Editorial Advisory Board, Microprocessors and Microsystems, Elsevier Publishers.
- Member, Editorial Board and Regional Editor for Southeast Asia for VLSI DE-SIGN: An International Journal of Custom-Chip Design, Simulation, and Testing Gordon and Breach Publishers.
- Co-Guest Editor, Special Issue of the Journal of Parallel and Distributed Computing on "Performance of Supercomputers", October 1993, Academic Press.
- Member, Editorial Board, International Journal of High Speed Computing, World Scienti fic Publishers.
- Member, Advisory Editorial Board of Computer-Aided Design, Butterworth Heinemann, Publishers.
- Member, Editorial Board, International Journal of Computer Aided VLSI Design, ABLEX Publishing Corporation.

B. Domestic

- Member, Editorial Board, Indian Journal of Pure and Applied Mathematics.
- Member, Editorial Board, IEEE India Journal, Horizons.
- Editor, Journal of The Indian Institue of Science, Bangalore.
- Editor, Computer Science and Informatics, Journal of the Computer Society of India.

Membership of Conferences

A. International

- 1. Chair, 1st International Conference on Metacomputing [IcoMec10) held at the National Institute of Oceanography, Goa on 16th and 17th December, 2010.
- 2. Member, Program Committee, International Conference on Bioinspired Optimization Methods and thier Applications (BIOMA-2004), Ljubljana, Slovenia, 14-15 October, 2004.
- 3. General Chair, Second International Conference on Information Technology (ITPC), Asian Applied Computing Conference (AACC), Kathmandu, Nepal, 29-31 Oct, 2004.
- 4. Member, Program Committee, The Seventh International Conference on Informa- tion Fusion in 2004(Fusion 2004), Stockholm, Sweden, 28 Jun 1 Jul, 2004.
- 5. Program Chair, North Asia-Pacific Computer Systems Architecture Conference (ACSAC 2004) Beijing, China, Sep 7-9, 2004.
- 6. General Chair, AACC 2004 (Second International Asian Applied Computing Conference), Kathmandu, Nepal, 4-6 June, 2004.
- 7. Co-Chair, ACM Workshop on Grid Computing and e-Science, 1-3 Aug, 2004, London.
- 8. Member, Advisory Board, International Conference on Signal Processing (ICSP), September 2003, Turkey.
- 9. General Chair, ITPC 2003 (International Conference on Information Technology Prospects and Challenges: In the 21 st Century), Kathmandu, Nepal, May 23-26, 2003.
- Member, Advisory Committee, ICANN/ICONIP 2003, (Joint 13th International Conference on Artificial Neural Networks and 10th International Conference on Neural Information Processing), 26-29 June, 2003, Istanbul, Turkey.
- Member, International Advisory Committee, IDEAL 2002 (Third International Conference on Intelligent Data Engineering and Automated Learning, 12-14 August, 2002, Manchester, UK), IDEAL 2003(Hong Kong), IDEAL 2004.
- 12. Member, International Organising Committee, Fusion 2002, (5th International Conference on Information Fusion) 8-11 July 2002, Washington DC, USA.
- 13. Member, Advisory Committee, ATM Interact Symposium 2001 on Broadband Networking in the New Millennium", The ATM Forum, August 22-24, 2001.
- 14. Member, Programme Committee, The Seventh Australasian Conference on Parallel and Real-Time Systems, PART'2000, Sydney, Australia, November 29-30, 2000.
- 15. Member, Advisory Committee, International Conference on Information Technology, December 2000.
- 16. Member, Program Committee, International Workshop on Grid Computing, GRID' 2000, Bangalore, India, December 17-20, 2000.
- 17. Member, Steering Committee, The Fourth International Conference/ Exhibition on High Performance Computing in Asia-Pacific Region, Beijing, China, May 14-17, 2000.
- 18. Member, Organising Committee, The Fourth World Multiconference on Systemics, Cybernetics and Informatics, SCI'2000, Orlando, Florida, USA, July 23-26, 2000.
- 19. Member, Program Committee, Asia-Pacific Symposium of Cluster Computing, APSCC'2000, Beijing, China, May 14-17, 2000.
- 20. Member, Program Committee, The Thirteenth International Conference on VLSI Design, VLSI'2000, Calcutta, India, January 3-7 2000.
- 21. Member, Advisory Committee, The Seventh International Conference on Advanced Computing and Communications, ADCOM'99, Roorkee, India, December 20-22, 1999.
- 22. Member, Advisory Committee, Conference on Information Technology CIT 99, Bhubaneswar, India, December 20-22, 1999.
- 23. Member, Steering Committee, Sixth International Conference on High Performance Computing, HiPC'99, Calcutta, India, December 17-20, 1999.
- 24. Member, International Program Committee, International Conference on Modelling and Simulation, Association of Modelling and Simulation Enterprises (AMSE), Leong, France, December 1999.
- 25. Member, International Program Committee, Sixth Annual Australasian Conference on Parallel and Real-Time Systems(PART'99), Melbourne, Australia, 29 November-1 December 1999.
- 26. Member, International Program Committee, Sixth International Conference on Neural

Information Processing, Perth, Australia, November 16-20, 1999.

- 27. General Co-Chair, Third International Conference on Computational Intelligence and Multimedia Applications, New Delhi, 23-26 September 1999.
- 28. Member, International Steering Committee, First IEEE Asia-Pacific Conference on ASICS, Seoul, Korea, 23-25 August 1999.
- 29. Member, Steering Committee, Seventh International Workshop on Parallel and Distributed Real-Time Systems (WPDRTS), San Juan, Puerto Rico, April 12-16 1999.
- 30. Member, Steering Committee, Twelth International Conference on VLSI Design, Goa, India, January 7-10 1999.
- 31. Tutorial Chair and Member, International Advisory Committee, International Conference on Evolutionary Computation in Engineering (ICECE'99), Chennai, India, January 6-9 1999.
- 32. Member, Organising Committee, International Conference on Information Technology Integration for Manufacturing, Bangalore, December 28-30 1998.
- 33. Member, Steering Committee, Fifth International Conference on High Performance Computing, HiPC 98, Madras, India, December 17-20 1998.
- 34. Member, Advisory Committee, The Sixth International Conference on Advanced Computing, Pune, India, December 14-16, 1998.
- 35. General Chair, First International Conference on Information Technology, Bhubaneswar, December 1998.
- 36. Member, Steering Committee, The Fifth Annual Australasian Conference on Parallel and Real-Time Systems PART'98, Adelaide, Australia, September 28-29 1998.
- 37. Member, International Steering Committee and Program Committee, Third High Performance Computing Asia Conference HPC - Asia 98, Singapore, September 23-25, 1998.
- 38. Member, Program Committee, 8th International Parallel Computing Workshop (PCW'98), Singapore, September 7-8, 1998.
- 39. Member, International Program Committee, Design of Information Systems for Manufacturing DIIMS'98, Fort Worth, Texas, USA, May 18-22 1998.
- 40. Member, Organizing Committee, International Conference on Nonlinear Dynamics and Brain functioning, Bangalore, Feb 5-11 1998.
- 41. Member, Steering Committee, Fourth International Conference on High Performance Computing, HiPC'97, Bangalore, India, December 17-20 1997.
- 42. Member, Program Committee and Advisory Committee, The Fifth International Conference on Advanced Computing, Chennai, India, December 15-17, 1997.
- 43. Member, Program Committee, IEEE Third International Conference on Algorithms and Architectures for Parallel Processing, Melbourne, Australia, December 8-12, 1997.
- 44. Member, International Advisory Committee, The Fourth International Conference on Neural Information Processing ICONIP'97, Dunedin/Queenstown, Newzealand, 24-28 November 1997.
- 45. Member, International Program Committee, Second IEE/IEEE International Conference on Genetic Algorithms in Engineering Systems: Innovations and Applications, University of Strathclyde, Glasgow, UK, 1-4 September 97.
- 46. Member, International Program/Steering Committee, Second High Performance Computing Asia Conference HPC-Asia'97, Korea, April 21-15 1997.
- 47. Member, Steering Committee, Third International Conference on High Performance Computing, HiPC'96, Trivandrum, India, December 19-22 1996.
- 48. Member, National Advisory Committee, IEEE ACE 96 XXII Annual Convention,"Quality A Global Perspective", Anna University, Madras, November 30 December 2, 1996.
- Member, Steering Committee and Program Committee, The Third Australasian Conference on Parallel and Real-Time Systems, 1996, Griffiths University, Brisbane, Queensland, Australia, 30 September - 1 October, 1996.
- 50. Member, Program Committee, Second International Conference on Design of Information Infrastructure Systems for Manufacturing, Eindhoven, September 1996.
- 51. Member, Program Committee, IEEE International Conference on Algorithms and

Architectures for Parallel Processing, Singapore, June 11-13, 1996.

- 52. Program Co-Chair, Ninth International Conference on VLSI Design (VLSI '96), Bangalore, India, January 3-6 1996.
- 53. Member, Program Committee, European Design Automation Conference (EDAC), Paris, France, March 11-14 1996.
- 54. Member, Steering Committee, International Conference on High Performance Computing, HiPC'95, New Delhi, India, December 27-30 1995.
- 55. Member, Program Committee, IEEE International Conference on Neural Networks, ICNN 95, Western Australia, 27 November-1 December 1995.
- 56. Member, International Programme Committee, The Second New Zealand International Two-Stream Conference on Artificial Neural Networks and Expert Systems ANNES '95, Dunedin, New Zealand, November 1995.
- 57. Member, Program Committee, The Second Australasian Conference on Parallel and Real-Time Systems (PART-95), Fremantle, Western Australia, September 28-29, 1995.
- 58. Member, Steering Committee, The Second Australasian Conference on Parallel and Real-Time Systems (PART-95), Fremantle, Western Australia, September 28-29, 1995.
- 59. Member, Steering Committee, High Performance Computing-Asia Conference, Taipei, Taiwan, September 18-22 1995.
- 60. Member, International Programme Committee, International Conference on Engineering Applications of Neural Networks, EANN '95, Helsinki, Finland, August 21-23, 1995.
- 61. Member, Technical Program Committee, IEEE IAS International Conference on Industrial Automation and Control, Hyderabad, January 5-7, 1995.
- 62. Member, International Advisory Panel, The IEEE First International Conference on Algorithms and Architectures for Parallel Processing, ICA3PP-95, Brisbane, Australia, 1995.
- 63. Member, Steering Committee, International Conference on Parallel Processing.
- 64. Program Co-Chair, First International Workshop on Parallel Processing, Bangalore, India, December 27-30 1994.
- 65. Member, Program Committee, 1994 International Conference on Parallel and Distributed Systems (ICPADSs '94), December 1994.
- 66. Member, Technical Committee and International Advisory Panel, Second IEEE Australian and New Zealand Conference on Intelligent Information Systems, ANZIIS 94, Brisbane, Australia, 29 November - 2 December 1994.
- 67. Member, Steering Committee, Sixth IEEE Symposium on Parallel and Distributed Processing, Oct. 1994, Dallas, Texas, USA.
- 68. Member, Organising Committee, Two-day Conference on Engineering in Software Design, September 1994.
- 69. Member, Program Committee, High Performance Computing Conference '94, Singapore, September 1994.
- 70. International Liaison Member, IEEE 1994 International Conference on Acoustics, Speech and Signal Processing, ICASSP '94, Adelaide, Australia, 19-24 April 1994.
- 71. Member Programme Committee, VLSI '94.
- 72. Member, Programme Committee, First Australia and New Zeland Conference on Intelligent Information Systems, ANZIIS-93, Perth, Western Australia, 1-3 December 1993.
- 73. Program Chair (Asia/Australia) IEEE Symposium on Parallel and Distributed Processing, Dallas, Texas, USA, December 1-4 1993.
- 74. Member, Programme Committee, Pacific Rim International December 1993.
- 75. Member, Programme Committee, First New Zealand International Two-Stream Conference on Artificial Neural Networks and Expert Systems ANNES '93, Nov 24-26, 1993.
- 76. Member, Asian Sub Committee, The First ACM International Conference on Multimedia ACM-MULTIMEDIA'93, Anaheim, California, USA, August 1-6, 1993.
- 77. Member, Organizing Committee and Publicity Africa, India, Pator Rim, Seventh International Parallel Processing Symposium.
- 78. Member, Programme Committee, International Conference on Computer Graphics (ICCG 93), February 22-26, 1993.
- 79. Member, Steering Committee, The Sixth International Conference on VLSI Design, Bombay,

India, January 3-6, 1993.

- 80. Member, National Programme Committee of the International December 7-11, 1992. Member, Programme Committee, Euromicro 92 Conference, Paris, France, September 1992.
- 81. Member, Program Committee, The Twenty-Second Annual International Symposium on Fault-Tolerant Computing, Boston, USA, July 8-10, 1992.
- 82. Member, Program Committee, CompEuro'92 1992 Engineering, The Hague, The Netherlands, May 4-8, 1992.
- 83. Program Committee, Sixth International Parallel Processing Symposium, Beverly Hills, California, USA, March 23-26 1992.
- 84. General Co-Chair, The Fifth International Conference on VLSI Design, Bangalore, India, January 4-7, 1992.
- 85. Member, Programme Committee, International Conference on Software Engineering, Bangalore, India, October 1991.
- 86. Member, Programme Committee, 1991 Paici Rim International Symposium on Fault -Tolerant Systems (PRFTS '91), Kawasaki, Tokyo, Japan, September 1991.
- 87. Member, Organising Committee, Indo-U.S. Workshop on CAD/CAM and Robotics, New Delhi, India, August 1991.
- 88. Member, International Advisory Committee, IEEE International Seminar of Region 10 (TENCON '91), New Delhi, India, August 28-30 1991.
- 89. Member, Program Chair, The Fourth International Conference on VLSI Design, India, January, 1991.
- 90. Member, Program Chair, The Third International Conference on VLSI Design, India, January, 1990.
- 91. Member, Advisory Committee, National Confrence on Information Security, Karunya Institute of Technology and Sciences, Coimbatore, 25-26 November, 2005.
- 92. Member, Advisory Committee, National Conference on Communications, Indian Institute of Technology, Kharagpur, 28-30 Jan, 2005.
- 93. Member, Advisory Committee, Fourth Indian Conference on Computer Vision, Graphics and Image Processing, Kolkata, 16-18 Dec, 2004.
- 94. Member, National Advisory Committee, National Conference on Information and Communication Sciences, Electronics and Computing, Chembarambakkam, Feb 6-7, 2004.
- 95. Member, Advisory Board, National Conference on Signal Processing, Intelligent systems, and Networking (SPIN 2003), Bangalore, December 4-5, 2003.
- 96. Member, National Organising Committee, National Seminar on Information and Network Security, Bangalore, August 2002.
- 97. Member, Advisory Committee, Computer Society of India Annual Convention CSI-2002, October 2002, Bangalore, India.
- 98. Member, National Organising Committee, National Seminar on Information and Network Security, Aug 2002.
- 99. Member, Organising Committee, All India Seminar on Technical Education Vision 2020, November 12 13, 1999, Roorkee.
- 100. General Chair, YUFORIC(Youth Forum in Computer Science and Engineering on Networking and Internet Conference), December 9-12 1998, Ahmedabad.
- 101. Member, Advisory Committee, Second Conference & Exposition on Petroleum Geophysics (SPG '98), January 19-21, 1998, Chennai, India.
- 102. Member, Steering Committee, Third National Conference on Neural Networks and Fuzzy Systems (NCNNFS'97), 23-25 July 1997, Chennai,India.
- 103. Conference Chair, Fourth Conference on Advanced Computing, Dec 1996, Bangalore, India.
- 104. Chairman, Program Committee, Computer Society of India Annual Convention CSI-96, Oct-Nov 1996, Bangalore, India.
- Member, Program Committee, First Conference on Fault Tolerant Systems, December 20-22, 1995, I.I.T. Madras, India.

- 106. Conference Advisory Chair, Third Conference on Advanced Computing, Dec 95.
- 107. Member, Steering Committee, National Conference on Neural Networks and Fuzzy Systems (NCNNFS '95), March 16-18, 1995, Madras, India.
- 108. Member, National Advisory Committee, Conference and Workshop on Afficial Intelligence and Neural Networks: A Unified Approach (AINN-95), Amritsar, February 1995.
- 109. Member, Program Committee, National Conference on Logics and Automated Reasoning (LAR), Hyderabad, October, 1994.
- 110. Member, Steering Committee, National Conference on Neural Networks from Foundations to Applications, NCNN'93.
- 111. Member, Organising Committee, Workshop on Parallel Processing, Bhabha Atomic Research Centre, Bombay.
- 112. Member, Organising Committee of the National Convention on "Knowledge Computing", September 1991.
- 113. Member, National Advisory Committee, Conference on Parallel Processing Technology and Applications PARCOM-90, CDAC Pune, December 1990.
- 114. Member, Organizing Committee, Seminar on Microprocessor-based Integrated Design of Industrial Systems, November 1989.
- 115. Member, Program Committee, Annual Convention of the Computer Society of India, September 1989.
- 116. Member, Program Committee for the Conferences on Foundations of Software Technology and Theoretical Computer Science, Pune, India, December 1987.
- 117. Member, Program Committee and Chairman, Technical Programme Committee for an Advanced Workshop on Fault-Tolerant Computing, July 1987.
- 118. Member, Programme Committee for an Introductory Workshop on Fault-Tolerant Computing, July 1986.
- 119. Member, Technical Program Committee, for the IEEE Seminar on "Distributed Computer Control and Monitoring Systems in Power Stations and Process Applications", New Delhi, India, November 1985.

Keynote/Invited Speaker

- Chief Guest and Keynote Speaker, IEEE Conference on Emerging Devices and Smart Systems, Mahendra Engineering College, Mahendhirapuri, Mallasamudram, Tamilnadu, 4 and 5 March 2016.
- Chief Guest and Keynote Speaker, National Conference on Emerging Trends in VLSI and Communication Engineering, Sree Sastha Institute of Engineering and Technology, Chennai, 19 Feb 2016.
- International Conference on Emerging Trends in Engineering Technology and Management 16-18 April,2015 S Group of Institutions, Dindigul, and Tamilnadu, India 16-18 April 2015.
- Inaugural Keynote addresses Research Students' Talk, NIT, Rourkela, 11 May 2015.
- Invited Talk on cluster, grid and cloud Computing Rourkela Chapter, CSI Rourkela Chapter, 12 May 2015.
- Invited Talk on Cyber Physical Systems, Veer Surendra Sai Technical University, Burla, Sambalpur, Odisha, 13 May 2015.
- Guest of Honour and Keynote Speaker, National Conference on Recent Innovations in Computer Networks (NCRICN'15), 21 Aug 2015, INTU Ananthpuram, Andhra Pradesh.
- Chief Guest and Inaugural Speaker, Pre-Conference Tutorials/Workshop, Third International Conference on 'Emerging Research in Computing Information Communication and Applications (ERICA-2015), 30 July 2015, Nitte Meenakshi Institute of Technology, Bangalore.
- Plenary Speaker, Electronics and Communications Engineering Track 16th National Conference on Technological Trends (NCTT 2015), Sep 11-12, 2015, Trivandrum.
- Chief Guest and Keynote Speaker IEEE International Conference on Communication Control and Intelligent Systems, 7-8 Nov 2015, GLA University, Mathura.
- Outstanding Paper Award, European Conference on Data Mining 2015.
- Chief Guest and delivered lecture, Wireless Networks, Awate College of Engineering, Pune, 12 Mar 10.
- Guest of Honour and delivered lecture, 'Glorious Globe Day', Institution of Engineers, Pune 9 Mar 2010.

- Chief Guest and delivered Inaugural Address, National Seminar on Information Technology, Simhagad Institute of Management and Computer Application, Pune, 20 Mar 10.
- Chief Guest and delivered Inaugural Address, National Conference on Global Management, DY Patil Institute of Management Studies, Pune, 14 Mar 2010.
- Chief Guest and delivered Inaugural Address, Training Programme on "Research Methodologies and Mathematical Modeling in Engineering & Technology, March, 11, 2010, Cummins College of Engineering for Women, Pune-52.
- Chief Guest and delivered lecture, 10th Annual Day, Shastri Group of Institutes, Pune, March, 13, 2010.
- Guest of Honour and delivered lecture, 21st Convocation Ceremony of IGNOU, Pune, March 16, 2010.
- Invited/Plenary Talk, International Conference on Emerging Trends in Computer Science, Communication and InformationTechnology (CSCIT2010), "Trends in Advanced Computing", 9-11 January 2010, Yeshwant Mahavidyalaya, Nanded, Maharashtra.
- Invited/Plenary Talk, 97th Indian Science Congress 2010, "Changing Paradigms in Information Technology (Wireless Technology and Computational Intelligence)", 3-7 January 2010, University of Kerala, Thiruvananthapuram.
- Invited Speaker, 2nd International Conference on Emerging Trends in Engineering & Technology (ICETET-09), 'Brain and Computer Interface is New Ray of Hope for Handicapped', 16-18 Dec 09, GH Raisoni College of Engineering, Nagpur.
- Invited Speaker, 44th Annual Convention of Computer Society of India, "Academic/Research : Challenges in Higher Technial Education in the Era of Global Competition, 8 Oct 09, VITS, Pune.
- Keynote Speaker, National Conference on "Wireless Technologies" (NCWT-08), 29-31 Dec 08, Padmashree D.Y. Patil Institute of Engineering & Technology, Pune.
- Keynote Speaker, 38th Annual Convention of ISTE and IFEES International Conference, 20 Dec 08, KIIT, Bhubneshwar.
- Invited Speaker, Symposium on Robotics and Autonomous Vehicles Views and Perspectives, 08 Nov 08, R&DE (Engrs), Pune.
- Invited Speaker, International Workshop on "Emerging Frameworks and Issues for S&T Recruitments", 19 Sep 08, RAC, Delhi.
- Invited Speaker, 41st Engineer's Day Celebration, 16 Sep 08, The Institution of Engineers (India), Pune.
- Invited Speaker, Workshop on Mobile Systems (WoMS-08), 11 Jul 08, West Bengal University of Technology, Kolkata.
- Keynote Speaker, "Advanced Course on VLSI Technology" organized by ISTE, 30 Jun 08, Bharati Vidyapeeth, Pune.
- Invited Speaker, National Workshop on C^4I^2SR , 24 Jun 08, HEMRL, Pune.
- Invited Speaker, Short Term Training Programme (STTP) on Advance Communication Systems, 23 Jun 08, VIIT, Pune.
- Invited Speaker, Workshop on "Parallelism Impact of Multicore and Latest Trends", 23 Apr 08, I²IT, Pune.
- Keynote Speaker, Diploma in Advance Computing Program, 21 Apr 08, KNOW-IT, Pune.
- Keynote Speaker, Prof. A K Choudhury Memorial Lecture, International Workshop on Distributed Computing, Dec 2005, Kharagpur.
- Keynote Speaker, National Conference on "Advanced Multimedia Processing and Applications", Oct 2005.
- Invited Speaker, National Conference on Information and Communication Engineering, Computing and Electronics, Feb 6 and 7 2004.
- Invited Speaker, Symposium on Arbitration and IT Industry: A Symbiosis, FKCCI and Indian Council of Arbitration, 20 Nov 2003, Bangalore, India.
- Invited Speaker, E-Governance Summit, IT.COM 2003, 4 Nov 2003, Bangalore, India.
- Invited Speaker, International Conference on Communication and Broadband Networking, May 5-7, 2003, Bangalore, India.
- Keynote Speaker, International Conference on Evolutionary Computing for Computers, Communication, Control and Power (ECCAP-2000), 4-7 Jan 2000, Chennai, India.
- Keynote Speaker, National Seminar on Artificial Intelligence, 27 Sept 1999, Bhubaneswar, India.
- Keynote Speaker, Seminar on Enterprise Resource Systems, 28 Sept 1999, Bangalore, India.
- Invited Speaker and Panelist, International Conference on Technical Education, Jan 98, Bhopal.
- Keynote Speaker, National Conference on Information Technology, Dec 97, Bhubaneswar.
- Keynote Speaker, National Convention on Networks and Information Systems, Ranchi, January 29 30, 1997.
- Invited Speaker, Conference of the Society for Petroleum Geophysicists, New Delhi, February 1996.

- Key Note Speaker, National Students' Convention Infoquest 95, Coimbatore, 1995.
- Key Note Speaker, National Seminar on Neural Networks, Ahmedabad, 1995.
- Key Note Speaker, Symposium on Genetic Algorithms, March 1995.
- Key Note Speaker, Conference on Software Quality Assurance, 1994.
- Platinum Jubilee Speaker, Computer Science Division, 81st Indian Science Congress, January 1994.
- Invited Speaker, Chemistry Division, 81st Indian Science Congress, January 1994.
- Invitee, Platinum Jubilee Lecture in the Section of Computer Sciences during the 81st Indian Science Congress.
- Invited Speaker, Indian Science Congress, January 1993.
- Invited Speaker, IEEE Conference on Computer Networking, Ahmedabad, Dec. 1992.
- Delivered a Plenary Talk on Massively Parallel Computation at the South East Asian Regional Computer Conference, Kuala Lumpur, Aug 11-14, 1992.

Session Chair/ Coordinator (upto 2010 only)

- Patron, National Seminar on "Nanomaterials for Devices : Characterization and Application", jointly by Poona University & DIAT, DU, Pune, 24-26 Jun 2010.
- Session Chair, International Conference on Nonlinear Dynamics and Brain functioning, Feb 5-11, 1998, Bangalore.
- Moderator, "Can we Build Competitive Architectures for General Purpose Applications?", HPC-ASIA'97 Conference, Seoul, Korea, 28 April-2 May 1997.
- Session Chair, HPC-ASIA'97 Conference, Seoul, Korea, 28 April-2 May 1997.
- Session Chair, Tenth International Conference on VLSI Systems (VLSI '97), Hyderabad, India, Jan. '97.
- Program Chair, Computer Society of India Annual Convention CSI-96, Oct-Nov 1996, Bangalore.
- Session Chairman, International Conference on Management of Data, Bangalore, Dec. 1992.
- Session Chairman, Technical Session on Interconnection Networks, Sixth International Parallel Processing Symposium, Beverly Hills, California, USA, March 23-26, 1992.
- Session Chair, Twenty Second Annual Symposium on Fault-Tolerant Computing, Boston, USA, July 8-10, 1992.
- Chairman, Technical Session on Vefication, Validation and Testing, International Conference on Software Engineering, October 1991.
- Session Chairman, IEEE International Conference of Region 10, TENCON'91, August 28-30, 1991, New Delhi.
- Chairman, Technical Session on Neural Network Architectures, 1991 International Symposium on Circuits and Systems, June 1991, Singapore.
- Chairman, Session on Distributed Systems/Computer Architecture/Networks, National Conference on Real-Time Systems, Indore, February 1991.
- Coordinator, The Fourth International Symposium on VLSI Design, New Delhi, January 1991.
- Coordinator, The Third International Workshop on VLSI Design, Bangalore, January 1990.
- Coordinator for a Tutorial on Parallel Processing Architectures and Algorithms, Annual Convention of the Computer Society of India, September 1989.
- Coordinator, National Symposium on Instrumentation, October 1989.

Membership of Professional Bodies

• Life Member, Orissa Information Technology Society.

- Member, New York Academy of Sciences.
- Member, Association for Computing Machinery.
- Member, International Computational Intelligence Society.
- Life Member, Member of Council, Indian Complex Systems Society.
- Life Member, Indian Science Congress Association.
- Life Member, National Telematics Forum.
- Member SIMULA Users' Group.
- Member IEEE VHDL Standards group.
- Member Association for Advancement of Fault Tolerant and Autonomous Systems and its Executive Committee.
- Life Member Instrument Society of India.
- Chairman IEEE Computer Society Chapter, Bangalore.
- Founder Member, Advanced Computing and Communications Society.
- Life Member VLSI Society of India.
- Member, Executive Committee, IEEE India Council.
- Responsible for starting IEEE Student Branch and IEEE Computer Society Branch Chapter at I.I.Sc., Bangalore, 1984; Member, Executive Committee of the IEEE, Bangalore Section, 1987.

Title of Ph.D Thesis Completed

- 1. Mrs Sunita V Dhavale, Digital Audio Watermarking Schemes for Copyright Protection, Jan 2015
- 2. Manisha J Nene, Uncovered Region Exploration Based Redeployment Algorithms for Mobile Wireless Sensor Networks, June 2014
- 3. Testing of Analog Circuits Built in Self Test (B K S L V Varaprasad, July 2007).
- 4. Design and Performance Evaluation of Efficient Datamining Algorithms (P. Deepa Shenoy, July 2005, Offcampus Nonformal Research Supervision).
- 5. 3D Image Reconstruction Using Optical Phase Retrieval and Cone-beam Tomography (Hemanth T, February 2005).
- 6. Perfomance Evaluation of High Compression High Fidelity Progressive Image Coding Algorithms (Vishwanath P Baligar, June 2003).
- 7. Genetic Programming based Multicategory Pattern Classification (J.Krishna Kishore, March 2001).
- 8. Parallel Tomographic Image Reconstruction on Hierarchical Bus-Based and Extended Hypercube Architectures (K Rajan, July 1997).
- 9. Algorithms for Test Generation and Fault Simulation of Path Delay Faults in Logic Circuits (Ananta Kumar Majhi, January 1996).
- 10. Evolutionary Approaches to VLSI Channel Routing (B.B. Prahlada Rao, November 1994).
- 11. Distributed Logic Simulation: Performance Studies of Event-Driven and Time-First Evaluation Algorithms (S. Sundaram, August 1994).
- 12. Genetic Algorithms: Novel Models and Fitness-based Adaptive Disruption Strategies (M. Srinivas, August 1993).
- 13. S-Nets: A Tool for the Performance Evaluation of Hard Real-Time Scheduling Algorithms (S. Balaji, April 1993).
- 14. Reliability and Performability Analysis of Distributed Memory Multicomputers (Samir.M. Koriem, April 1993).
- 15. Escape Mechanism in Bistable Systems Driven by Strongly Correlated Noise (T.G. Venkatesh, Feb 1993).
- 16. A Specification Environment for Distributed Real-Time Systems (Rajib Mall, April 1992).
- 17. Dynamical and Type 2 Characterization of Ideal Basis Reduction in Symbolic Algebraic Computation (Chandan Haldar, June 1992).
- 18. Architecture, Performance and Applications of a Hierarchical Networks of Hypercubes (J. Mohan Kumar, Feb 1992).
- 19. Systolic Architectures for Realistic 3-D Graphics (Praxetes Canutes Mathias, Dec 1989).
- 20. Diagnostic Reasoning with Surface and Deep Level Knowledge in Medical Domain

(S.V.R.K. Prabhakar, Jan 1989).

- 21. Concurrency Control Algorithms for Data base Systems Exploiting the Semantics of Read only Transactions (Ramesh Chandra Hansdah, Dec 1988).
- 22. Data Flow Implementation of Functional Languages Supporting Fault-Tolerance & Resource Management (R. Govindarajan, Dec 1988).
- 23. Formal Tools for Specification Driven Protocol Design of Distributed Computing Systems (V.K. Agrawal, March 1988).
- 24. Nondeterminism and Communication in Functional Programming Systems A study in
- 25. Formal Program Development (Asis K. Goswami Sep 1985).
- 26. Self-Tuning Control of Multivariable & Hammerstein Nonlinear Systems (K. Anbumani, Oct 1981).

Teaching Activities Significant Graduate Courses

- 1. Topics in Mobile Computing Technologies
- 2. Discrete Structures
- 3. Computer Organization and Design
- 4. Computer Architecture
- 5. Computer Graphics
- 6. Database Management Systems
- 7. Computing Devices and Systems
- 8. Hybrid and Real-Time System
- 9. Computer Control of Industrial Processes
- 10. Basics of VLSI Systems
- 11. Computer Aided Design of VLSI Systems
- 12. Modeling and Simulation of Digital Systems

Significant Under-Graduate Courses

- 1. Discrete Structures
- 2. Engineering Applications of Computers
- 3. Pulse and Digital Circuits
- 4. Industrial Electronics and Control

Industrial Consultancy and Sponsored Research

- Principal Investigator of a Sponsored Research Project, "Experimental and Computational Studies on Neurobiological Systems", Department of Biotechnology, Government of India (Rs.162.60 lakhs).
- Co-Principal Investigator of a Sponsored Research project, "Serial and Parallel implimentation of Space-Time Adaptive Processing (STAP) Algorithm ", Centre for Airborne Systems, Department of Defence, Government of India (Rs.4,98,000).
- Principal Investigator of a Sponsored Research Project, "Signal Processing Algorithms for Brain Images", National Brain Research Centre, Gurgaon (Rs.1,98,000).
- Co-Principal Investigator of a Sponsored Research project, "Feasibility study of eal-Time Processing of Synthetic Aperture RADAR Images using Space Time Adaptive Processing Algorithms", Centre for Airborne Systems, Department of Defence, Government of India (Rs.4,94,000).
- Co-Investigator of a Sponsored Research project, "Study and Simulation of Interconnection Network and Architecture" for ANUPAM, BARC Parallel Processing System, Bhabha Atomic Research Centre, Board of Research in Nuclear Sciences, Department of Atomic Energy, Government of India (Rs.14,49,000).
- Principal Investigator of a Sponsored Research Scheme, "Neural Network-Based Automatic Target Recognition", Defence Research and Development Laboratory (DRDL), Department of Defence, Government of India (Rs.8, 07,900)
- Principal Investigator of a Sponsored Research Scheme "Simulation Studies for the Design and Development of Neurocomputers", Department of Biotechnology, Government of India (Rs.39.90 lakhs).

- Principal Investigator of a Sponsored Research Scheme "Neural Network-Based Data Fusion for Airborne Radar Data Processors", Centre for Airborne Systems, Department of Defence, Government of India (Rs.2,99,000).
- Principal Investigator of a Sponsored Research Scheme "UNIX on Massively Parallel Systems: A Technical Survey", Centre for Development of Advanced Computing, Pune (Rs.3,70,000).
- Chief Investigator of a sponsored research project "Transputer-Based Neural Network Simulator", sponsored by the Centre for Development of Advanced Computing, Pune (Rs.2,04,900).
- Co-Investigator of a sponsored research project "Development of Application Software Packages and User Training in Hybrid Computation", sponsored by the Department of Electronics, Government of India (Rs. 30.00.000).
- Principal Investigator of a research project on "Research and Training in Advanced Microprocessors and Multi-microprocessor Systems", sponsored by the Ministry of Human Resources Development, Department of Education, Government of India (Rs.25,00,000).
- Principal Investigator of a research project on "Research and Training in High Performance Computer Aided Design Systems for Mechanical and Electronic Applications" sponsored by the Ministry of Human Resources Development, Department of Education, Government of India (Rs.25,00,000).
- Principal Investigator of a research project on "PASIST: parallel Architectures for Simulation Studies" sponsored by Bhabha Atomic Research Centre, Electronic Division, Bombay (Rs.5,83,000).
- Principal Investigator of a research project on "Testbed for Fault-Tolerant Dis-tributed Computing Systems for Space Applications" Government of India, Department of Space, ISRO Head Ouarters, Cauvery Bhavan, Bangalore (Rs.5,20,000).
- Principal Investigator of a sponsored research project "Formal Model for Task Execution in Real-Time Distributed Computing Systems", sponsored by Indo-French Centre for the Promotion of Advanced Research (Rs.13,60,000).
- Principal Investigator of a sponsored research project "Multiprocessor Systems for Neural Computing" sponsored by the Department of Science & Technology, Government of India (Rs.25,90,560).
- Investigator of a discretionary research project on "Systolic Architecture for 3D Graphics" funded as part of the research scheme, "Knowledge Based Computer Systems", sponsored by the Department of Electronics. Government of India and UNIDO.
- Consultant to Centre for the Development of Advanced Computing and Tata Unisys Ltd., Bangalore. Consultant to Computer Division, Electronics Corporation of India Ltd. Hyderabad, for a project on Feasibility Study and System Design for Computer Control of LD Converters".
- Consultant to Messrs. Larsen and Toubro Limited for a project entitled "Simulation Evaluation of the Control System Design for a Carriage and Trolley System for On-Line Nuclear Fuel Rod Positioning Control for a Bhabha Atomic Research Center Reactor".

Visits Abroad (upto 2010 only)

- Visited Sri Lanka, Colombo to attend "EDUCON A Symposium of Vice-Chancellors/Directors of Indian Universities/Institutions", organised by The EDUCON (a common platform for dialogue and intense interaction between all the people involved in betterment of education scenario across India), 21-23 Aug 2010.
- Visited Bankok, Thailand to attend the 'Symposium of Vice-Chancellor/Directors of Indian Universities/leading institutions", held from 20th to 23rd August, 2009, organised by The EDUCON (a common platform for dialogue and intense interaction between all the people involved in betterment of education scenario across India) at Bankok, Thailand.
- Visited Beijing and Shanghai to attend the Ninth General Conference and Twentieth Anniversary Celebrations of the TWAS (Oct 2003). Visited Bath, Bristol, London, and York to attend EPSRC-sponsored Workshops in the area of Ubiquitous
- Computing and deliver seminars in this area (Sep 2003).
- Visited University of Central Florida, Florida International University, and University of Miami, USA, Nov 2002.
- Visited The Chinese University of Hong Kong, The International Institute of Software Technology, The UN University (July-Aug 2002).
- Visited University of New Castle upon Tyne, University of Manchester, University of Surrey in UK as part of the Royal Society - Indian National Science Academy exchange programme (Jan-Feb 2001).

- Visited the National Institute of Mental Health, Bethesda, Washington, USA, July-Aug 2000.
- Visited the National Institute of Melhar Health, Bethesda, Washington, OSA, July-Aug 2000. Visited Asian Institute of Technology, Bangkok, Thailand to deliver seminars in the areas of Evolutionary Computing and Mobile Computing (March 2000). Visited Lousiana State University, Baton Rouga, USA and Intel, Portland, USA to deliver seminars in the areas of Mobile Computing and to receive the IEEE Computer Society's Technical Achievement Award at the IEEE Board of Governors Meeting in Portland, USA, (November 1999).
- Visited Singapore to attend the High Performance Computing Asia 98 Conference, to deliver seminar at the National University of Singapore, University of Putra, Kaula Lumpur; Penang Skill Development Centre, Penang to deliver seminars (September 98).
- Visited the Maui High Performace Computing Centre, Maui, Hawaii, USA; University of Hawaii,
- Visited the Math High Performace Computing Centre, Math, Hawah, USA, University of Hawah, Honolulu, National University of Singapore, and attended Asia and South HaciDesign Automation Conference with EDA Techno Fair'98, Yokohama, Japan (February 98).
 Visited Sung Kyun Kwan University, Ajou University in Seoul, Korea; Beijing University of Aeronautics and Astronautics, Institute of Computational Mathematics and Schengineering Computing, Chinese Academy of Sciences in Beijing, China (May 97).
 Visited Maui High Performance Computing Centre in Hawaii, Aizu University, Waseda University, NTT Usited Maui High Performance Computing Centre in Hawaii, Aizu University, Waseda University, NTT
- LSI Labs, and Toshiba in Tokyo; University of Hong Kong and Hong Kong University of Science and Technology, Cirkisys Technology Ltd in Hong Kong (July 96).
- Visited Laboratoire MASI, University of Paris, France, University of North Texas Denton, Dallas, USA, Cambridge University and Oxford University, UK (1993).
- Visited University and Oxford University, OK (1993). Visited University of California Irvine, University of Southern California, University of California Los Angeles, Texas A & M University, University of Florida Gainsville, University of North Carolina Raliegh, University of Oklahoma, Laboratoire MASI, University of Paris, France, University of Massachusetts Amherst, Georgia Institute of University, Monash University, Commonwealth Scienflic and Industrial Research Organisation (CSIRO), Australia (1992)
- Visited IEEE Sections of Brisbane, Auckland, Melbourne, Adelaide, and Singapore as fithet IEEE Region 10 Distinguished Lecturer for 1990; Asian Institute of Technology, Bankok; University of Melbourne;University of Waikato, NeWZealand; National University of Singapore; Monash University, and Crisholm Institute of Technology, Australia; Chulalongkorn University, Bangkok; James Cook University of North Queensland, Townsville, Australia (1990).
- Visited University of Illinois, Urbana; Oakland University, Detroit; General Motors, Detroit; University of Windsor, Canada; Rutgers University, New York; State University of New York, Buffalo; University of Massachusetts, Amherst; NASA Ames Research Laboratory, California; IBM Thomas J Watson Research Laboratory, New York; and The Ohio State University, Columbus; all in U.S.A. (1989).
- Visited Jozef Stefan Institute, Edvard Kardelj University Ljubljana, Yugoslavia; Hungarian Academy of Sciences Budapest Hungary, and ETH Zurich Switzerland, as part of an Academic Exchange Programme of the Indian National Science Academy (1987).

Significant Continuing Education Programmes Organised and/or Taught

- 1. Software Engineering
- 2. **Computer Graphics**
- 3. Computer Organization and Architecture
- 4. System Design for Computer-Aided Automation
- Design of Microprocessor-Based Systems 5.
- 6. Hybrid Computation and Simulation
- 7. Introduction to Microprocessors
- 8. **Digital System Design**
- 9. **Computer Networks**
- 10. CAD of Printed Circuit Boards
- 11. Computer Graphics and PCs
- 12. Advanced Microprocessor Systems

Laboratory Development Activities

Directed the development of Instrumentation, Digital Systems, Microprocessors and Hybrid Computer laboratories.

Service to the Profession

Membership of other Technical Committees

A. International

- PhD Thesis Examiner, University of Malaya, Kualalumpur, Malayasia, September 2015.
- Member, International Advisory Board, 4th International Symposium on Computaional Business Intelligence (ISCBI16), Olten, Switzerland, Sep5-7, 2016.
- Member, Editorial Board, International Journal of Cognitive Biometric, Inderscience Publishers.
- Member, IETE Journal of Education, Published by Taylor and Francis.
- Advisory Board Member and keynote Speaker, international Conference on Advanced Computing and Intelligent Engineering (ICACIE-2016), C V Raman College of Engineering, Bhubaneswar, 21-23Dec 2016.
- Honorary Advisory Chair, International Conference on Computational Intelligence in Data Mining(ICCIDM-2015), Bhubaneswar, 5-6 Dec 2015.
- Member CSIR Engineering Sciences Committee.
- Member Technical Committee IEEE Advanced Information Technology Electronics and Automation Conference, (IAEAC 2015), Chongqing, China 19-20 Dec 2015.
- General Chair, International Conference on Emerging Trends in Electrical Communication and Information Technologies 20-22 Dec 2015, Sri Ramanujan Institute of Technology, Ananthpuram, Andhra Pradesh.
- General Chair, 9th International Conference on Advanced Computing and Communication Technologies, 27-29 Nov 2015, Asia Pacific Institute of Technology, Panipat.
- Member Technical Advisory Committee, Siksha Anusandhan, Bhubaneswar.
- Member Advisory Committee Third International Conference on Emerging Research in Computing Information Communication and Applications (ERICA 2015), 31ST July 1 Aug 2015.
- Honorary Advisory Chair, International Conference on Computational Intelligence in Data Mining (ICCIDM 2015), Bhubaneswar, Odisha, Dec 26-27, 2015.
- Session Chair, Regional Conference of Young Scientists on Nano Science and Nano Technology, 18-20 Feb 2015, JNCASR, TWAS.
- Member International Advisory Board, 3rd International Symposium on Computational and Business Intelligence (ISCBO 15), 7-8 Dec 2015, Bali, Indonesia.
- Member Technical Program Committee Track on "Power Aware Design", 29th International Conference on VLSI Design and 15th International Conference on Embedded Systems, 4-8 Jan 2016, Kolkata.
- Member Selection Committee University of Agricultural Sciences, Dharwad, 26-27 June 2015.
- Member Selection Committee, Deptt of Computer Science, Central University, Punjab, 1st July 2015.
- Member Research Advisory Board, SRM University, Chennai.
- Reviewer, Research Grants Council (RGC), Hong Kong.
- Member International Board of Editors, IETE Journal of Education, Published by Taylor & Francis.
- Auditors, TEQIP Project, TKM College of Engineering, Quilon.
- Chair, Discussion Meeting, National Institute of Advanced Studies, 20May 2015.
- Member, Academic Advisory Committee of TEAM TECH (Total Engineering, Analysis and Manufacturing Technologies) 2006, 2007, 2008, 2009, International Conference and Ehhibition.
- Member Program Committee of E-Science 2006, Amsterdam, December 4-6, 2006.
- Member, Technical Committee of FUSION 2006, Florence, Italy.
- PC-Member, HPC and Computer Architecture Conference, Brazil, 2005.
- General Chair, Third International Conference on Innovative Applications of Information Technology for Developing World, Asian Applied Computing Conference (AACC), Kathmandu, Nepal December 10-12,2005.
- General Chair, Third International Conference on Intelligent Sensing and Information Processing, Bangalore, December 2005.
- General Chair, Twelfth International Conference on Advanced Computing and Communication, Ahmedabad, December 2004.
- General Chair, Asian Applied Computing Conference, Kathmandu, October 2004.
- Member, Program Committee, Second Workshop on High Performance GridCom-puting (HPGC'05), Denver, Colorado, USA, April 2005.
- Member, The Shared Hierarchical Academic Research Computing Network (SHARCNET) High Performance Computing Strategic Council, Canada, 2003.

- Member, International Advisory Committee, International Conference on Intelligent Sensing and Information Processing, Chennai, 4-7 January, 2004.
- Member, Programme Committee, International Conference on Recent Trends and New Directions of Research in Cybernetics and Systems Theory, Guwahati, 1-3 Jan, 2004.
- Member, International Advisory Committee, International Conference on Recent Trends and New Directions of Research in Cybernetics and Systems Theory, Guwahati, 1-3 Jan, 2004.
- Member, Programming Committee, 4th International Workshop on Grid Computing (Grid 2003), Phoenix, Arizona, 17 Nov, 2003.
- Member, International Advisory Board, International Twelfth Turkish Symposium on Artificial Intelligence and Neural Networks, Turkey, July 2003.
- Member, Advisory Committee, International Conference on Communication and Broadband Networking (ICBN) ATM Interact Symposium, 2003.
- Member, International program Committee, International Conference in Manufacturing Excellence 2003, Melbourne, Australia, 20-22 Oct, 2003.
- Member, International Advisory Committee, Eleventh International Conference on Neural Information Processing, 2003.
- Member, International Advisory Committee, HPC Asia, 2002.
- Member, International Advisory Board and Technical Programme Committee, Canakkale Onsekiz Mart University, Canakkale, Turkey, 2002.
- Member, Program Committee, SIPS 2001.
- Member, Program Committee, Grid 2001, International Workshop on Grid Computing, Denver, Colorado, USA, November 2001.
- Reviewer, Research Proposals, Research Grants Council, University Grants Committee, Hong Kong and CSIRO, Australia.
- Member, Executive Committee, Youth Forum in Computer Science and Engineering (YUFORIC) Task Force, IEEE Computer Society, 1998-1999, Engineering Standards Group.
- International Advisory Committee, Special Interest Group on Artificial Neural Networks and Expert Systems, New Zealand Computer Society.
- Member, IEEE Fellowship Evaluation Committee, IEEE Computer Society, 1994.
- Member, Executive Committee and Coordinator, ASIA-PACIFIC RIM, IEEE Technical Committee on Parallel Processing, 1993, 1995-1998.
- Member, Technical Committee, IEEE Circuits and Systems VLSI Systems and Application Technical Committee.
- Member, Indo-U.S. Forum for Cooperative Research and Technology Transfer (IFCRTT).
- Reviewer for IEEE Transactions on Computers, IEEE Transactions on Software Engineering, IEEE Transactions on Knowledge and Data Engineering, IEEE Transactions on Circuits and Systems, IEEE Computer, Journal of Parallel and Distributed Computing, The Computer Journal, Proceedings of the IEE, Journal of Microcomputer Applications, Microprocessors and Microsystems, Euromicro 1988 Conference, Eighth International Conference on Applied Algebra, Algebraic Algorithms and Error Correcting Codes, IEEE International Conference on Computers, Systems and Signal Processing, Conference on Foundations of Software Technology and Theoretical Computer Science, IFIP/CSI Conference on CAD/CAM/CAE for Industrial Applications.
- Member, Technical Resource Experts, IEEE Standards Evolutionary Computation Working Group.

B. Domestic (upto 2010 only)

- Senate Member, University of Pune, 2010.
- Chairman, 'International Conference on Quality Higher Education in Basic Sciences', Zaheer's Science Foundation, New Delhi, 2010.
- Member, Dr. Dara P Antia Memorial Lecture Series Committee, Indian Institute of Metals, Pune, 2010.
- Member, Research & Recognition Committee (Computer Studies Department), Symbiosis International, Deemed University, Pune, 2010.

- Chairman, AICTE South Central Regional Committee, 2010.
- Member, Advisory Committee, Symbiosis College of Information Technology, Pune, 2010.
- Member, National Advisory Committee for Restructuring of Academic Programme in Educational Sciences, NIT, Rourkela, 2009, 2010.
- Member, Selection Committee for Recruitment, IIT Ropar, Rupnagar, Punjab, 2010.
- Member, HR Consultative Body, DRDO HQ, New Delhi, 2009, 2010.
- Member, Academic Advisory Committee, IIT, Bhubaneswar, 2009, 2010.
- Member, AICTE/NBA Engineering Accreditation Committee, 2009, 2010.
- Member, Inter-Academy Advisory Panel for DST's "Innovation in Science Pursuit for Inspired Research (INSPIRE) Programme, 2009, 2010.
- Member/Visitor's Nominee, Selection Committee for Professors, Aligarh Muslim University, Delhi, 2009, 2010.
- MHRD Nominated Evaluator, National Project Implementation Unit, NOIDA, 2009, 2010.
- Member, Board of Governors, NIT, Durgapur, 2009, 2010.
- Member, Governing Council, NITTE Meenakshi Institute of Technology, 2005-2010.
- Member, Board of Governors, Fourth Annual Convocation of the National Institute of Technology, Durgapur, 2008, 2009, 2010
- Member, Subject Expert Committee on Engineering Sciences of FIST Program, DST, 2009.
- Member, National Board of Accreditation, 2008, 2009.
- Member, HR Council, RAC, DRDO, 2008, 2009.
- Member/Visitor's Nominee, Selection Committee for Professors, IIT, Kharagpur, 2009.
- Member/Visitor's Nominee, Selection Committee for Professors, IIT, Roorkee, 2009.
- Member, Prof. Yash Pal Committee for Higher Education, 2008, 2009.
- Member, Planning Commission, Higher Education, 2009.
- Chairman, Expert Committee, National Board of Accreditation, Birla Institute of Technology, Mesra, Ranchi, 17-20 Oct, 2008.
- Member, UGC/AICTE Review Committee, 2008-09, New Delhi
- Member, Academic Council, National Institute of Technical Teachers' Training & Research, Chandigarh, 2008.
- Member, Society of National Brain Research Centre, New Delhi, 2008.
- Member, FIST Program 2008, Indian National Science Academy, New Delhi, 2008.
- Member, Committee on Engineering Sciences for selecting candidates for Swarnajayanti Fellowship, Department of Science & Technology, Government of India.
- Member, State Council on Science & Technology, Orissa.
- Member, Governing Body of Orissa Computer Application Centre (OCAC), Bhubneshwar, 2007-08.
- Member, Expert Committee on Engineering Sciences, Department of Science & Technology, New Delhi, 2007, 08, 09.
- Member, Governing Body of International Institute of Information Technology, Bhubneshwar, 2007, 08, 09.
- Member, FIST Program 2006, Second Meeting for Presentation of the Screened & Evaluated new Proposals under Engineering Sciences Area, Indian National Science Academy, New Delhi, 19-20 Sep, 2006.
- Chairman, Expert Committee on Ubiquitous Computing Research Initiative, Department of Information Technology, New Delhi, 2006.
- Member, UGC Expert Committee for granting Deemed University status to Symbiosis Institute of Computer Studies and Research, Pune, 2001.
- Member, Advisory Committee, National Conference on Recent Advancements in Information Technology, Sri Ramakrishna Engineering College, Coimbatore, Tamilnadu, Feb 9-10, 2007.
- Member, Academic Advisory Council, IEC Collegge of Engineering and Technology, Lucknow.
- Member, Governing Council, Kuppam Engineering College, Andhra Pradesh, Mar 25, 2006.

- Mentor and Auditor, World Bank Project on Technical Education Quality Improvement Programme.
- Steering Committee Chair, Thirteenth International Conference on Advanced Computing and Communications ADCOM 2005, Amrita Vishvavidyapitham, Coimbatore, (Tamil Nadu) Dec 14-17.
- Member, Advisory Board, Invited Speaker, National Conference on Modern Trends in Information and Communication Technology, Mullana, Ambala, Haryana, October 2005.
- Convener, One Day Workshop on Use of Kannada in Information Technology, Bangalore, July 2005.
- Technical Program Chair, Two-Day Seminar on Emerging Trends and Challenges in Technical Education, Bangalore, November 2004.
- Member, Engineering Sciences research Committee, CSIR (Council of Sciencei and Industrial Research).
- Member, panel of Experts, Research Council, Regional Research Laboratory, Council of Scientic & Industrial Research, Jorhat, Assam, 2004-2006.
- Member, Academic Council, National Institute of Technical Teachers Training, Chandigarh.
- Member, Council, Indian National Science Academy, 2004-2006.
- Member, Special Committee, School of Computer and Systems Sciences, Jawaharlal Nehru University, New Delhi, 2003-2006.
- Member, Research Council, National Aerospace Laboratories, 2001-2004.
- Member, Visiting Committee, University of Mysore, 2003.
- Member, International Advisory Committee and Program Committee, International Conference on Recent Trends and New Directions of Research in Cybernetics and Systems Theory, 2003.
- Member, Steering Committee on Ubiquitous Computing, Department of Information Technology, Software Development Division, 2003.
- Visitor's Nominee, Selection Committees for IIT Madras, Guwahati, Roorkee, and Delhi, 2000-2003.
- Member, Standing Purchase Committee for South Western Regional Office, AICTE, 2001.Member, Committee of Karnataka State Centre of the Institution of Engineers (India), Computer Engineering Division, 2000-2004.
- Member, Academic Sub-Committee, All India Institute of Speech and Hearing, 2000.
- Member, Sectorial Committee for Computer Science and IT for the National Board of Accreditation (NBA).
- Member, Governing Council and Steering Committee, CDAC.
- Member, Board of Directors, Leo Infotech (P) Ltd., Bangalore.
- Member, VLSI R&D Advisory Committee, Semiconductor Complex Ltd., Chandigarh.
- Member, Governing Council, ITI Ltd.
- Member, Governing Council, Indian National Academy of Engineering, 2001-2002.
- Member, Academic Council, Advanced Training Institute for Information Technology Pvt. Ltd., Madras.
- Member, Academic Sub-Committee, All India Institute of Speech and Hearing, Mysore, Ministry of Health and Family Welfare, Govt. of India.
- Member, Fellowship Scrutiny Committee, Physical Sciences for the year 2000, The National Academy of Sciences, India
- Distinguished Expert, The Mahindra Institute of Software Engineering and Management, Pune.
- Member, Sectional Committee, Indian National Academy of Engineering, 1996-98.
- Member, Council, Indian National Academy of Engineering, 1999-2000.
- Member, Academic Advisory Council, entranceguru.com Private Limited, Chennai.
- Member, Executive Board, BPL Innovision, Bangalore.
- Honorary Advisor, M S Ramaiah School of Advanced Studies, Bangalore.
- Member, Task Force for Computer Education Programme Expansion in Karnataka.
- Member, Executive Board, BPL Telecom Limited, Bangalore.
- Member, Board of Directors, Federal Technologies Ltd., Bangalore.
- Member, Senate (Academic Committee), Indian Institute of Information Technology, Allahabad.
- Member, Governing Body, National Brain Research Centre (NBRC), New Delhi.
- Member, Computer Engineering Division, The Institution of Engineers (India), 1999-2000.
- Honorary Advisor, Gokul Education Foundation.
- Member, Governing Council, Indian National Academy of Engineering.
- Chairman, Board of Governors, Indian Institute of Science and Information Technology, Bhubaneswar.
- Member, Board of Governors, Bhubaneswar Institute of Management and Information Technology

(BIMIT), Bhubaneswar.

- Member, AICTE Inspection Committees for New Engineering Colleges in Tamilnadu and Karnataka.
- Member, Project Advisory Committee (PAC), Integrated Long Term Programme of Cooperation in Science and Technology between India and Russia (ILTP) in the area of COMPUTER AND ELECTRONICS, Dept. of Science and Technology, Government of India.
- Member, Technical Advisory Board, Kalinga Institute of Industrial Technology, Bhubaneswar, Orissa.
- Member, Advisory Committee, Computer and Communication Sciences Division, Indian Statistical Institute, Calcutta, India.
- Member, Technical Committee, Eleventh Indian Engineering Congress, Bangalore, Dec 96.
- Member, Advisory Committee, National Seminar on High Performance Systems, May 1995, India.
- Member, Peer Review Committee in Life Sciences, University of Pune
- Member, Review Committee, Computer Science and Engineering Programs, Indian Institute of Technology, New Delhi
- Member, Undergraduate Board of Studies in Computer Engineering, Mangalore University, Mangalore, India
- Member, Board of Studies in Computer Science and Engineering, Government College of Technology, Coimbatore, India
- Member, Academic Council, University of Hyderabad, India
- Member, Management Advisory Committee for the Young Scientists Programme, Department of Science and Technology, Government of India
- Member, Steering Committee on Neurosciences, Department of Biotechnology, Government of India.
- Member, Project Review and Steering Group (PRSG) for the Technology Mission Programme Project on "Design and Development of an Advanced Communications Co-Processor", C-DAC, Pune
- Chairman, Selection Committee for the selection of the awardees of Senior Research Fellowship and Research Associateship in Engineering, Council of Statenta Industrial Research (CSIR), Government of India.
- Member, Scientific Council, ASTROVED CENTER: Astrophysics, Space, Solar & Vedic Studies Center, Jaipur.
- Member, National Advisory Committee, Rajiv Gandhi National University of Computer Science, New Delhi.
- Member, Review Committee, On-board Computer Configurations for Launch Vehicles, Vikram Sarabhai Space Centre, Thiruvantapuram, India.
- Member, Standing Advisory Committee, Integrated Review of all the Masters level Programme in the area of Computer Science & Engineering, Computer Technology and Computer Applications.
- Member, Advisory Board, Tata Unisys Ltd. Education Services Activity.
- Member, State Council on Science and Technology, Government of Orissa.
- Chairman, Executive Committee, Indian Transputer Users' Group (ITUG).
- Member, Asian Subcommittee, ACM Multimedia '93 Symposium.
- General Chairman, CDAC's Assessment Committee of their Staff Members in the Member Technical Staff Category.
- Member, Standing Advisory Committee, Computer Science, Cochin University of Science and Technology.
- Member, Governing Body, Orissa Computer Application Centre, Bhubaneswar.
- University Representative, Planning and Evaluation Committee, Coimbatore Institute of Technology, Bharathiar University, Coimbatore.
- Member, Engineering Sciences Research Committee, Council of Sciefiti and Industrial Research (CSIR), Government of India.
- Member, Expert Committee, Foreman Training Institute, Ministry of Labour, Government of India.
- Member, Panel of Experts, Indian National Scientific Documentation Centre, New Delhi.
- Member, Technical Advisory Committee, Centre for Development of Electronic Systems, CSIR, Madras.
- Convenor, Accredition Committee set by the Department of Electronics, Gov-ernment of India, for Review of Computer Science Curricula in Various Private Institutions in India.
- Member, Technology Development in Indian Languages, Department of Electronics, Government of India, New Delhi.

- Member, Technical Advisory Committee for the Appropriate Automation Promotion Programme Centres, Department of Electronics, Government of India, New Delhi.
- Member, Academic Council, Lan Eseda Computer Centre, Bangalore.
- Member, Expert Committee for the Examination Unit of Joint CSIR-UGC Examination.
- Member, Technical Panel on Photonics Computing, Department of Electronics, Government of India, New Delhi.
- Member, Information and Computer Science Research Committee, The Council of Schenti and Industrial Research.
- Member, Advisory Committee, VLSI Design Centre, Semiconductor Complex Ltd., Bangalore.
- Convener, Institutional Networking Project on Microprocessor-Based System Design, funded by the Ministry of Human Resources Development, Government of India, at the Regional Engineering College, Hazratbal, Srinagar.
- Convener, Institutional Networking Project on Computer Networks, funded by the Ministry of Human Resources Development, Government of India, at the Jawaharlal Nehru Technological University, Hyderabad.
- Reviewer for Defence Science Journal, Journal of the Indian Institute of Science, Journal of the Institution of Electronics and Telecommunications Engineers, CSI Journal 'Computer Science and Informatics', National Conference on Machines and Mechanisms.
- Member, Working Group on Parallel Computing Technology Development, Department of Electronics, Government of India.
- Member, Review group for the Parallel Processing Project at the Centre for Development of Telematics (CDOT).
- Member, Review Committee for the Centre for Development of Advanced Computing's project on Architecture I Target Machine.
- Member, University Grants Commission's Expert Committee to review Computer Science and Technology programmes in the Indian Universities.
- Member, Technical Advisory Committee for the Microprocessor Engineering Applications Centre set up by UNDP and Department of Electronics, Government of India, at the Steel Authority of India Limited, Ranchi.
- Member, Programme Committee for the Computer Aided Design centre set up by UNIDO and Department of Electronics, Government of India, at the Indian Institute of Science.
- Associate Coordinator for the above CAD centre.
- Member, Programme Committee for the Centre for Microprocessor Applications set up by the Ministry of Education, Government of India, at the Indian Institute of Science (till November 1983).
- Member, Selection Committees at ISRO Satellite Applications Centre, National Institute for Mental Health and Neurosciences, Centre for Development of Telematics, Union Public Service Commission, Defence Research and Development Organization, Bharat Electronics Limited, and Banaras Hindu University, Orissa University of Agriculture and Technology and Indian Institutes of Technology at Kanpur, Madras, and Bombay.
- Member, Executive Committee for the DEC 1090 Central Computing facility of the Indian Institute of Science, 1982-84.
- Member, Organizing Committee for the Continuing Education Programme on Software Engineering.
- Guest faculty member at Texas Instruments India Ltd, Regional Engineering College, Rourkela, Steel Authority of India Ltd., Wipro Systems Ltd., Hindustan Aeronautics Ltd., Department of Space, National Productivity Council, Power System Training Institute, Indian Institutes of Technology, Bharat Heavy Electricals Ltd. and Institution of Electronics and Telecommunications Engineers; in the areas of Computer Graphics, Parallel and Distributed Computing Systems, fixial Intelligence, CAD CAM, Personal Computers and Database Management Systems.
- External Examiner for Sambalpur, Madras, Mysore and Andhra Universities, Indian Institutes of Technology, Delhi, Bombay, Madras, and Kharagpur, and Indian Institute of Management, Calcutta.
- Member, Research students' selection committees, Staff selection committees, computer procurement committees and curriculum development committees for Computer Science and Engineering, at the Indian Institute of Science.
- In-Charge, Central Hybrid Computing Facility, Indian Institute of Science, Bangalore, during 1980-

1982.

- Member, Advisory Committee for the M.Sc. Computer Science Programme of the Madurai Kamaraj University.
- Member, Library Committee, Indian Institute of Science, Bangalore.
- Chairman, Study Board in Computer Science, Kerala University.
- Member, Special Committee, School of Computer and Systems Sciences, Jawaharlal Nehru University, New Delhi.

LIST OF PUBLICATIONS

PROF. LM PATNAIK

International Journals

1.	Sejal Desai, Vinuth Chandrasheker, Vijay Mathapati, Venugopal K Rajuk, Sundaraja S Iyengar, Lalit M
1.	Patnaik, "User Feedback Session with Clicked and Unclicked Documents for Related Search
	Recommendation", IADIS International Journal on Computer Science and Information Systems, ISSN :
	1646-3692, vol. 11, no. 1, pp. 81-98, 2016.
2.	Sejal D, Shailesh K G, Tejaswi V, Dinesh Anvekar, Venugopal K R, S S Iyengar, and L M Patnaik, "Query
	Recommendation based on Query Relevance Graph", Transactions on Machine Learning and Data Mining,
	ibai-publishing, Germany, ISSN: 1865-6781, ISBN: 879-3-942952-41-5, vol. 9, no. 1, pp. 3-26, 2016.
3.	S Raghavendra, C M Geeta, Rajkumar Buyya, K R Venugopal, S S Iyengar, and L M Patnaik. "DRSMS:
	Domain and Range Specific Multi-Keyword Search over Encrypted Cloud Data", in International Journal of
	Computer Science and Information Security (IJCSIS), vol. 14, no. 5, May 2016. (Accepted and listed in
	Thomson Reuters) ISSN: 1947 5500
4.	S Raghavendra, Nithyashree K, C M Geeta, Rajkumar Buyya, K R Venugopal, S S Iyengar, and L M
	Patnaik. "RSSMSO Rapid Similarity Search on Metric Space Object Stored in Cloud Environment"
	in International Journal of Organizational and Collective Intelligence (IJOCI), IGI-Global Publisher,
	USA, vol. 6, no. 3, pp. 32-47, 2016. (Accepted and listed in ACM Digital Library) ISSN: 1947-
	9344, EISSN: 1947-9352
5.	S Raghavendra, Doddabasappa P A, C M Geeta, Rajkumar Buyya, K R Venugopal, S S Iyengar, and L M
	Patnaik, "Secure Multi-Keyword Search and Multi-User Access Control over an Encrypted Cloud Data" in
	International Journal of Information Processing(IJIP), vol. 10, no.2, 2016.(Accepted) ISSN: 0973-8215
6.	G Prathima E, Shiv Prakash T, Venugopal K R, S S Iyengar, L M Patnaik. "SAAQ : Secure Aggregation for
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7.	Sejal D, Kamalakant T, Dinesh Anvekar, Venugopal K R, S S Iyengar, L M Patnaik, "Webpage
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8.	Sunita V Dhavale, Rajendra S Deodhar, Debasish Pradhan and L M Patnaik, A Robust Zero Watermarking
	Algorithm for Stereo Audio Signals, International Journal of Information and Computer Security,
	Inderscience Publishers
9.	Poornima G, Vinod, K Suresh Babu, K B Raja, K R Venugopal, and L M Patnaik, "Graded Reliance Based
	Routing Scheme for Wireless Sensor Networks," International Journal of Communication Technology for
	Social Networking Services, ISSN: 2287-8467, vol. 3, no. 2, pp. 1-16, May 2015.
10.	Arunalatha J S, Suvarna C, Venugopal K R, L M Patnaik, "FCHE: Face Classification using Histogram
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11.	Kumaraswamy M, Shaila K, Tejaswi V, Venugopal K R, S S Iyengar and L M Patnaik, "Multihop Multi-
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	Computer Engineering (IOSR-JCE), e-ISSN: 2278-0661, p-ISSN: 2278-8727, vol. 17, no. 2, pp.01-10,
	April 2015.
12.	S Kumaraswamy, Manjula S H, K R Venugopal, L M Patnaik, "P ² DM-RGCD: PPDM Centric
	Classification Rule Generation Scheme", In Global Journal of Computer Science and Technology (GJCST),
	USA, Online ISSN: 0975-4172 Print ISSN: 0975-4350, vol. 15, no. 2, pp. 1-9, March-2015.
13.	Lata B T, Vidya Rao, Sivasankari H, Shaila K, Venugopal K R, L M Patnaik, "SEAD: Source Encrypted
	Authentic Data for Wireless Sensor Networks", International Journal of Engineering Research and
	Development (IJERD), e-ISSN : 2278-067X, p-ISSN : 2278-800X, vol. 11, no. 3, pp. 1-16, March 2015.

14.	Bhavya S, Shaila K, Venugopal K R and L M Patnaik, "A Study on Issues and Challenges to Achieve
	Better QoS in Wireless Sensor Networks", International Journal of Engineering Sciences and Research
	Technology (IJESRT), ISSN: 2277-9655, vol. 4, no. 3, pp. 308-317, March 2015.
15.	Arunalatha J S, Rangaswamy Y, Tejaswi V, Shaila K, Raja K B, Dinesh Anvekar, Venugopal K R, S S
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	Engineering Research and Application, ISSN 2248-9622, 5(3), (Part -1): 20-31, March 2015.
16.	B S Shruthi, Shaila K, Venugopal K R and L M Patnaik, "The Paradigm of Major Issues and Challenges in
101	Wireless Sensor Networks", International Journal of Advanced Research in Computer Science and Software
	Engineering (IJARCSSE), ISSN:2277-128X, vol. 5, no. 3, pp. 601-609, March 2015.
17.	Prabha R, Prashanth Kumar Gouda, Manjula S H, K R Venugopal and L M Patnaik, "MTADF: Multi hop
	Traffic Aware Data Forwarding for Congestion Control in Wireless Sensor Networks", International
	Journal of Wireless and Mobile Networks, ISSN: 0975-4579, vol. 7, no. 1, pp. 107-118, February 2015.
18.	Pushpa C N, Arpit Singh Baghel, Thriveni J, Venugopal K R and L M Patnaik, "Taxi Passenger
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19.	Lata B T, Raghavendra M, Sumukha T V, Suhas H, Tejaswi V, Shaila K, Venugopal K R, L M Patnaik,
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20.	Manjunath swamy B E, Appaji M Abhishek, Thriveni J, Venugopal K R, L M Patnaik, "Multimodal
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21.	Abhilash C N, Chandan A S, Manjula S H, Venugopal K R, Patnaik L M, "A Progressive Approach to
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22.	Arunalatha J S, Prashanth C R, Tejaswi V, Shaila K, Raja K B, Dinesh Anvekar, Venugopal K R, S S
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24.	Tanuja R, S H Manjula, K R Venugopal, L M Patnaik, "Secure and Privacy Preserving Data Centric Sensor
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25.	Prabha R, Krishnaveni M, Manjula S H, K R Venugopal and L M Patnaik, "TAEER: Trust Aware Energy
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	Engineering (IJISME), ISSN: 2319-6386, vol. 3, no. 4, January 2015.
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27.	Lata B T, Raghavendra M, Sumukha T V, Suhas H, Tejaswi V, Shaila K, Venugopal K R, L M Patnaik,
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20	vol. 2, no. 1, pp. 1-6, January 2015.
28.	G Sunilkumar, Thriveni J, Venugopal K R and L M Patnaik, "A Novel Approach to Detect Malicious User
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29.	Technology (E), ISSN 0975-4172, vol. 14, no. 2, pp. 29-44, 2015 Yamuna Devi C R, Saishiva K, Sunil Kumar, S H Manjula, K R Venugopal, L M Patnaik, "Trust-Based
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30.	MJ. Nene, RS Deodhar and LM Patnaik, Algorithm for Autonomous Reorganization of Mobile Wireless
50.	Camera Sensor Networks to Maximize Coverage, To appear in IEEE Sensors Journal. Vol. 15, No. 8
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31.	Prabha R, Shivaraj Karki, Manjula S H, K R Venugopal and L M Patnaik, "Quality of Service for
	Differentiated Traffic Using Multipath in Wireless Sensor Networks", International Journal of Inventive
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32.	MJ. Nene, R S Deodhar and LM Patnaik, DP-UREA: An Algorithm for Autonomous Reorganization of
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33.	Sunita V. Dhavale, Rajendra S. Deodhar, Debasish Pradhan and L. M. Patnaik, "State Transition Based
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37.	Sunita V. Dhavale, Rajendra S. Deodhar, Debasish Pradhan and L. M. Patnaik, "Fuzzy Logic based
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38.	Sunita V. Dhavale, Rajendra S. Deodhar and L. M. Patnaik, "Lossless Audio Watermarking based on the
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43.	Lata B T, Raghavendra M, Tejaswi V, Shaila K, Venugopal K R, S S Iyengar and L M Patnaik, "SSEGR:
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44.	Arunalatha J S, Suvarna C, Tejaswi V, Shaila K, Raja K B, Dinesh Anvekar, Venugopal K R, S S Iyengar,
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